

ABSTRACT OF THE DISCLOSURE

A field emission display includes a first substrate. At least one gate electrode is formed in a predetermined pattern on the first substrate. A plurality of cathode electrodes is formed on the first substrate in a predetermined pattern. At least one first insulation layer is formed between the at least one gate electrode and the plurality of cathode electrodes. Emitters are mounted within openings of the cathode electrodes formed in the cathode electrodes. A second insulation layer having a plurality of channels is formed on the cathode electrodes such that the emitters are positioned within the channels. At least one focusing electrode is formed on the second insulation layer. A second substrate is provided opposing the first substrate with a predetermined gap therebetween. At least one anode electrode is formed on a surface of the second substrate opposing the first substrate. Phosphor layers are formed on the anode electrode in a predetermined pattern.